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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

ISODA

Art Unit: 2816

Application No.: 09/811,456

Examiner: L. Nguyen

Filed: March 20, 2001

Attorney Dkt. No.: 108075-00056

For: IMPROVED INPUT BUFFER CIRCUIT FOR SEMICONDUCTOR DEVICES

AMENDMENT UNDER 37 CFR § 1.121

Commissioner for Patents
Washington, D.C. 20231

April 3, 2003

Sir:

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TECHNOLOGY CENTER 2800

In response to the outstanding Office Action dated December 3, 2002, having been duly extended from March 3, 2003, until April 3, 2003, by the attached Petition for Extension of Time, please amend the application as set forth below.

IN THE CLAIMS:

Please amend claims 19, 38 and 43-47 as follows. A copy of the marked-up original claims is attached to this response showing the changes made therein as set forth in 37 C.F.R. § 1.121.

19. (Twice Amended) An input buffer circuit comprising:
a differential amplifier circuit, disposed between a first power supply and a second power supply, for receiving first and second input signals and generating an amplified signal corresponding to a voltage difference between the first and second input signals;

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